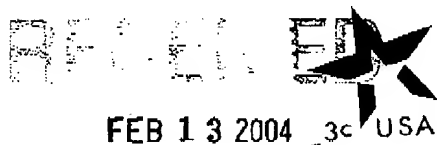
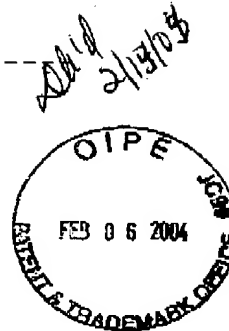


ONS00149
09/817,330

Appendix A

Copy of Return Postcard for Formal Drawing SubmittalON Semiconductor
Law DepartmentLydia McNamara
Patent Administration - A700
ON Semiconductor
P.O. Box 62890
Phoenix, AZ 85082-2890ATTY. DKT. NO: ONS00149
SUBMITTAL OF FORMAL DRAWINGS-----
Group: 2827
IN RE APPLICATION OF Phil Celaya
APPLN. NO. 09/817330
FILED: 3/26/2001
FOR: METHOD OF MAKING A LEAD-FREE
INTEGRATED CIRCUIT PACKAGEEnclosed is 3 SHEETS of formal
drawingsKJ:lmc
Mail Date: 2/3/2004

A-1

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